

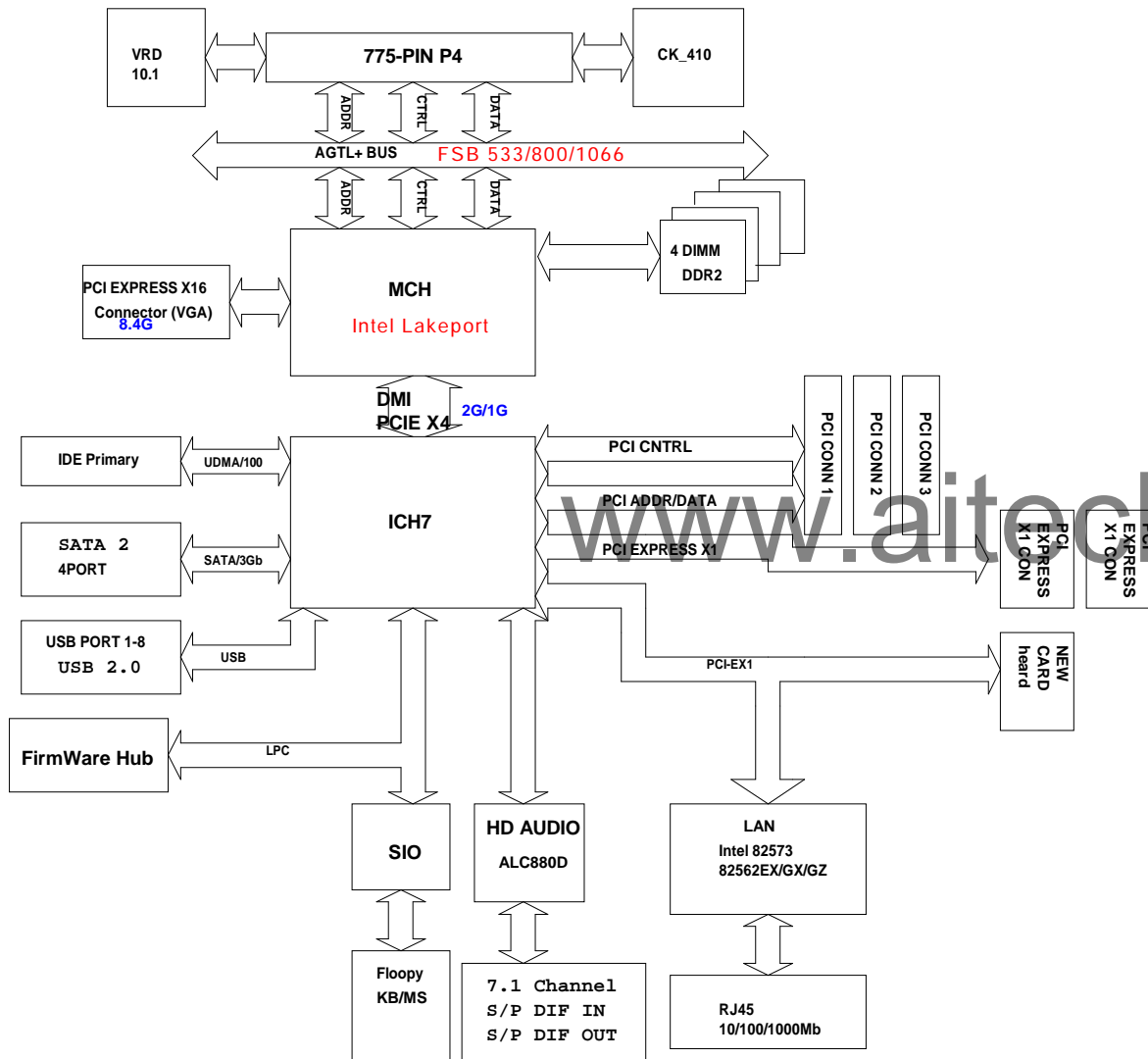
Intel APAC Lakeport CRB Schematic

Version: 1.4

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BLOCK DIAGRAM



ICH7 GPIO SETTING

PINNAME	WELL	USAGE	I/O	NOTE
GPI0	MAIN	P_REQA	I	
GPI1	MAIN	P_REQ5	I	
GPI2	MAIN	P_REQE	I	
GPI3	MAIN	P_REQF	I	
GPI4	MAIN	P_REQG	I	
GPI5	MAIN	P_REQH	I	
GPI6	MAIN	N/A	I	
GPI7	MAIN	N/A	I	
GPI8	RESUME	LPC_PME	I	
GPI9	RESUME	66DET	I	
GPI10	RESUME	FWH_WP	I	
GPI11	RESUME	SMB_ALERT	I	
GPI12	RESUME	ACZ_DET	I	
GPI13	RESUME	N/A	I	
GPI14	RESUME	N/A	I	
GPI15	RESUME	N/A	I	
GPI16	MAIN	N/A	O	
GPI17	MAIN	N/A	O	
GPI18	MAIN	N/A	O	
GPI19	MAIN	N/A	O	
GPI20	MAIN	N/A	O	
GPI21	MAIN	N/A	O	
GPI22	MAIN	N/A	OD	
GPI23	MAIN	LDQR1#	O	
GPI24	RESUME	LAN_DISABLE	I/O	
GPI25	RESUME	N/A	I/O	
GPI27	RESUME	N/A	I/O	
GPI28	RESUME	N/A	I/O	
GPI32	MAIN	N/A	I/O	
GPI33	MAIN	N/A	I/O	
GPI34	MAIN	N/A	I/O	

PCI DEVICE SETTING

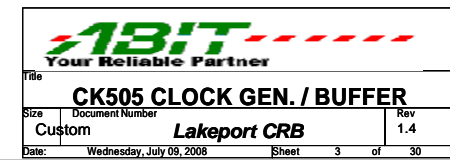
DEVICE	PCI1	PCI2	PCI3
IRQ	F	G	F
	G	F	E
	H	E	H
	E	H	G
ID	21	22	23
REQ/GNT	0	1	2

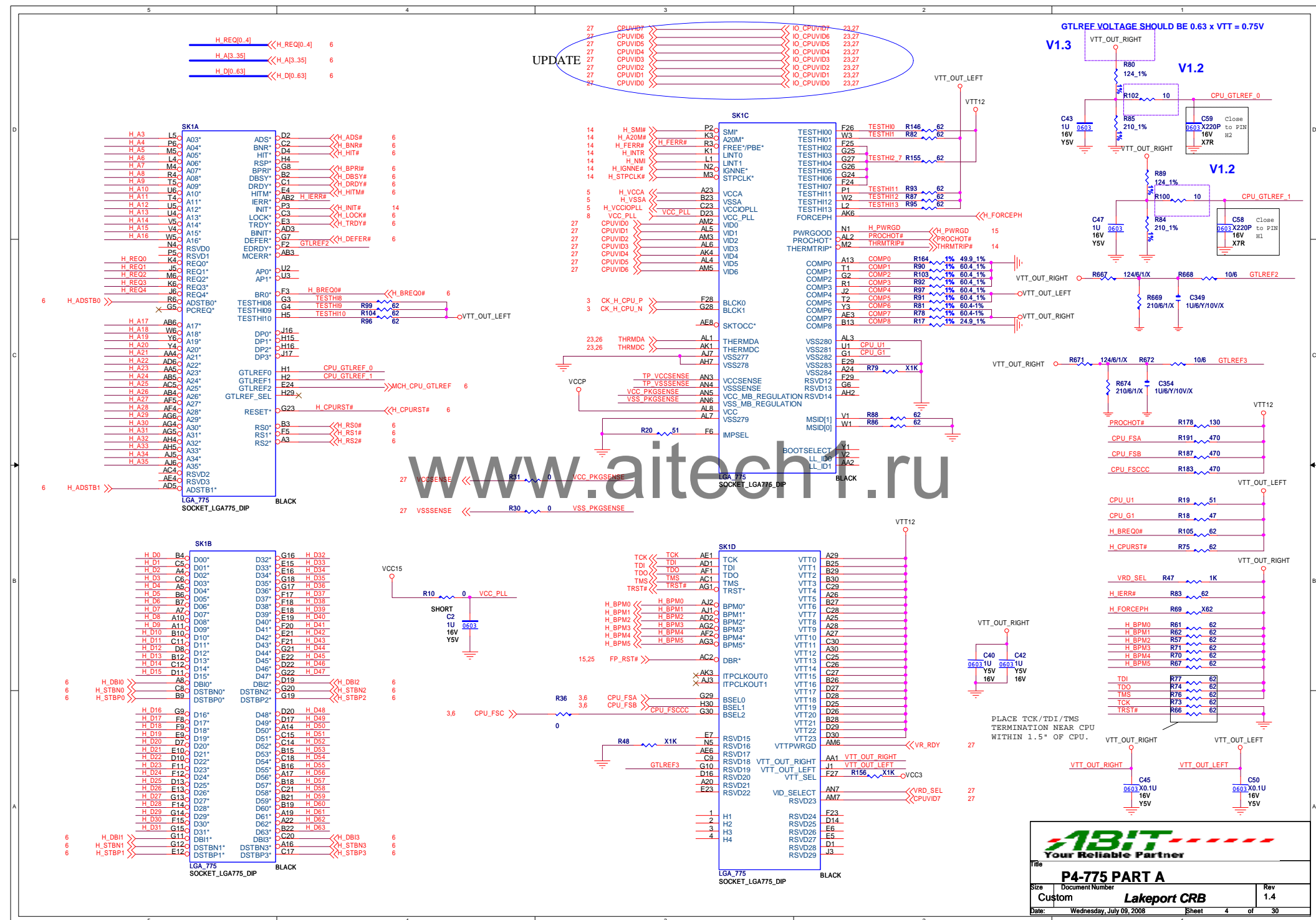
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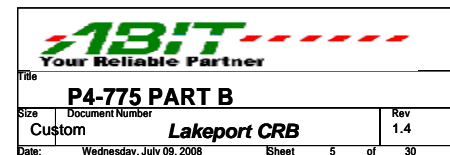
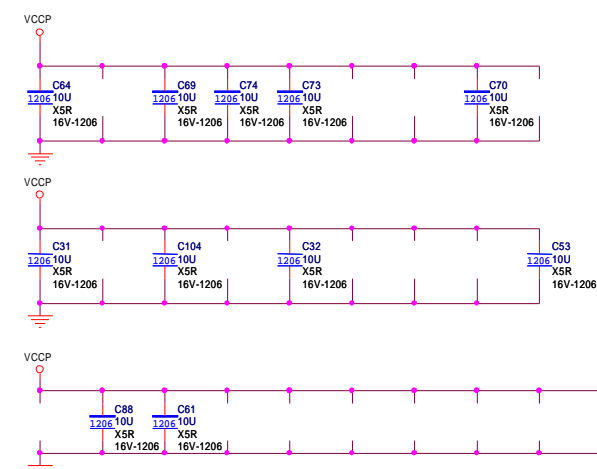
BLOCK DIAGRAM & SETTING

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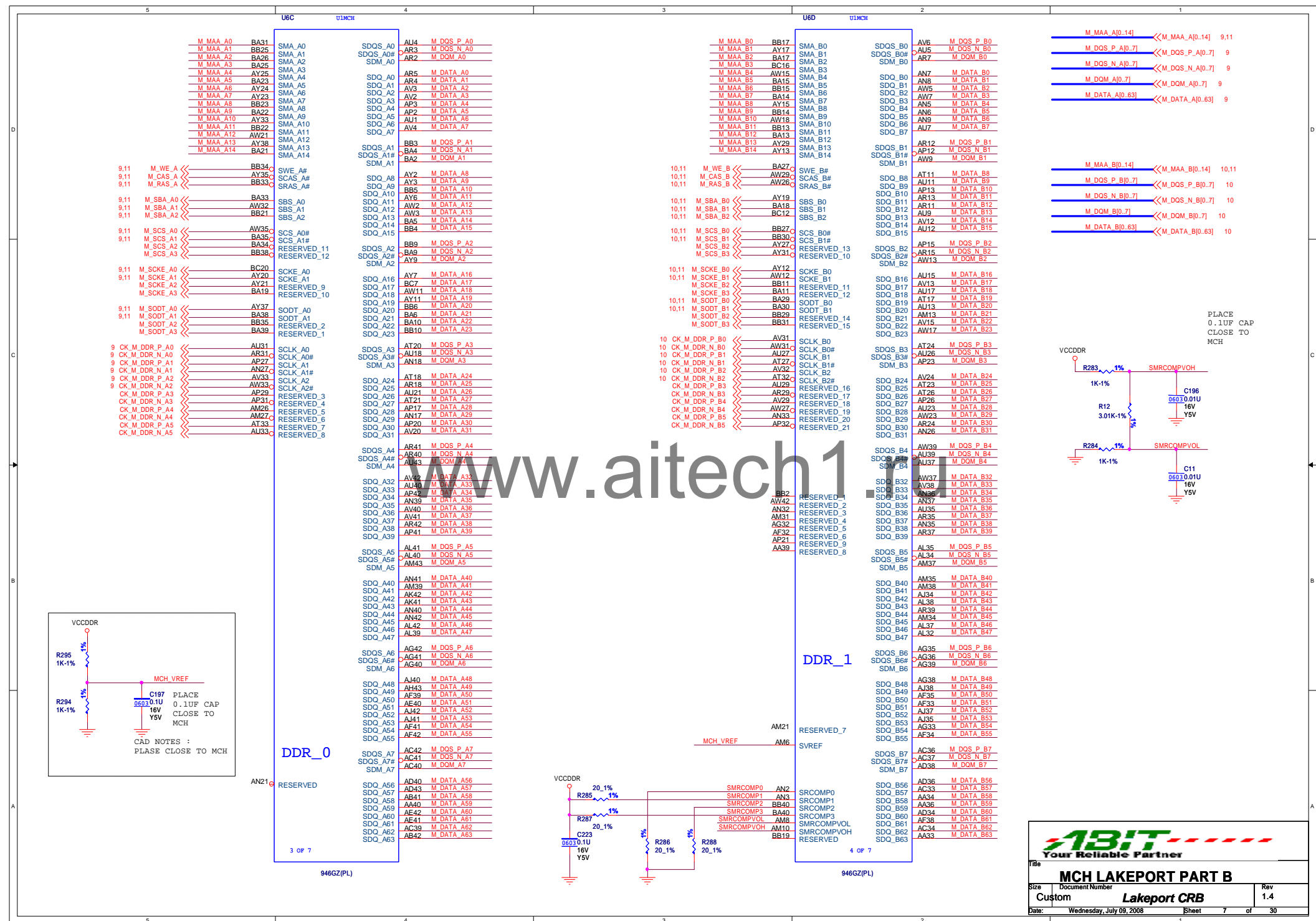
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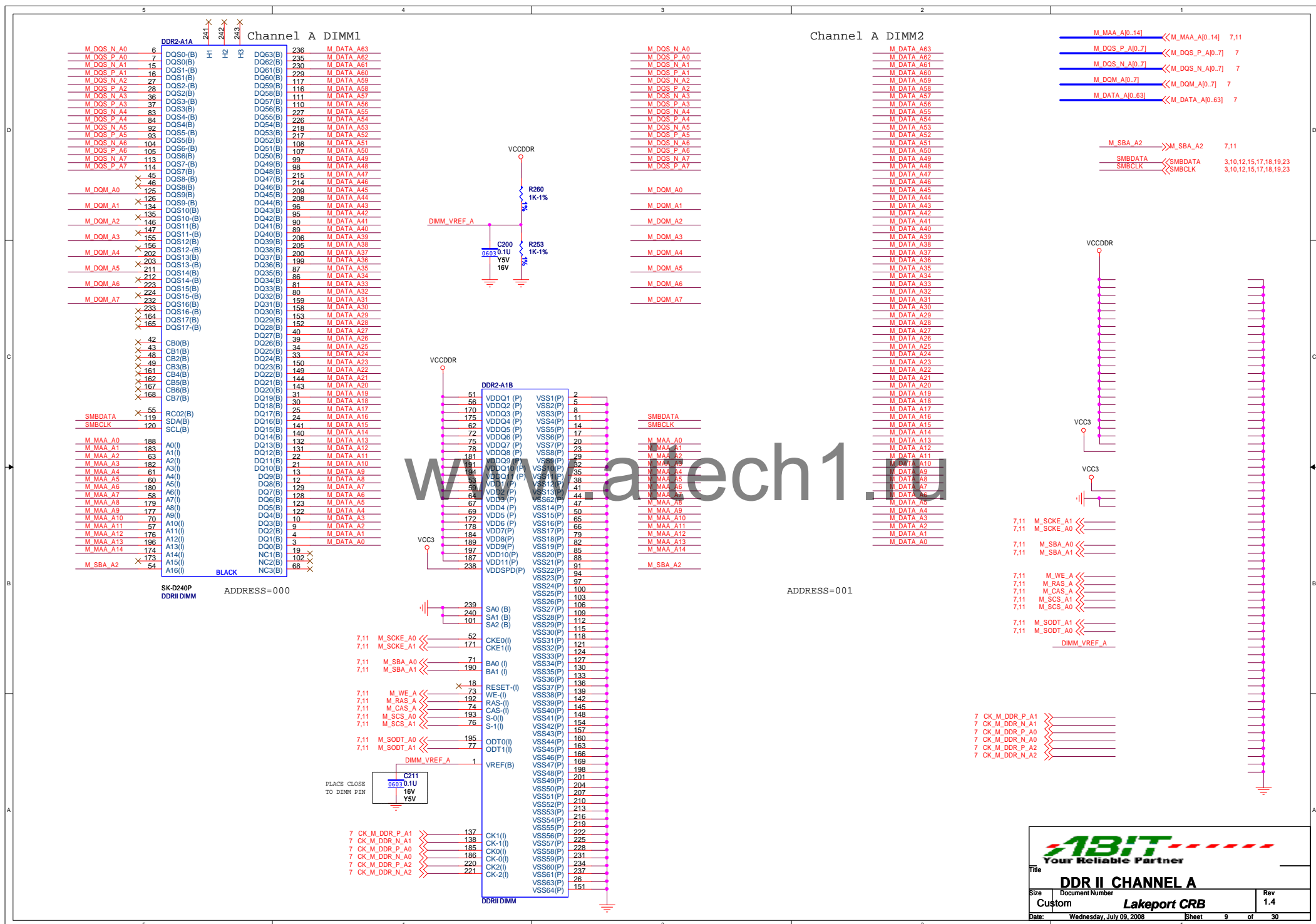


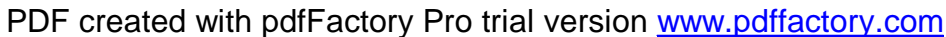


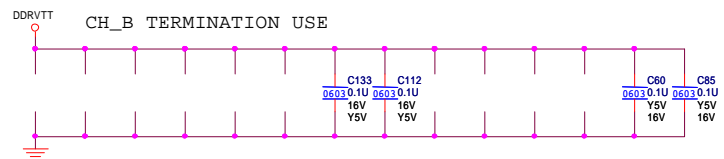
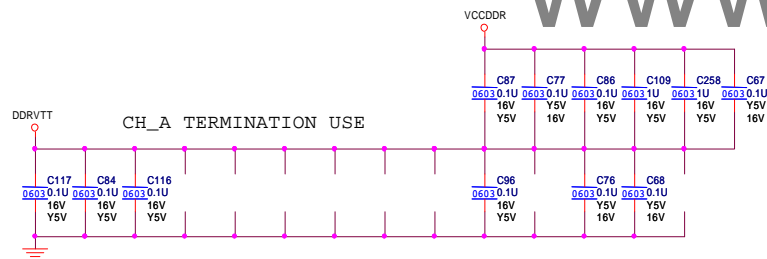
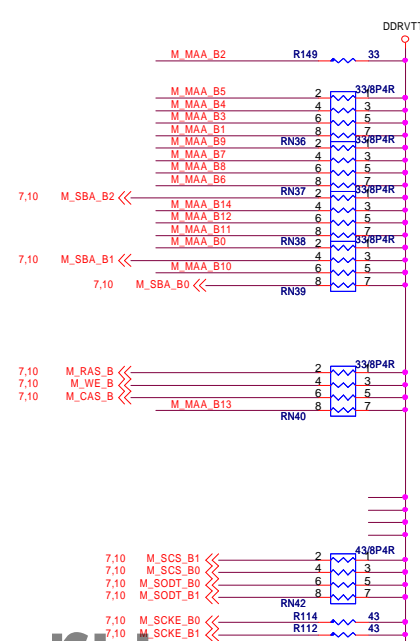
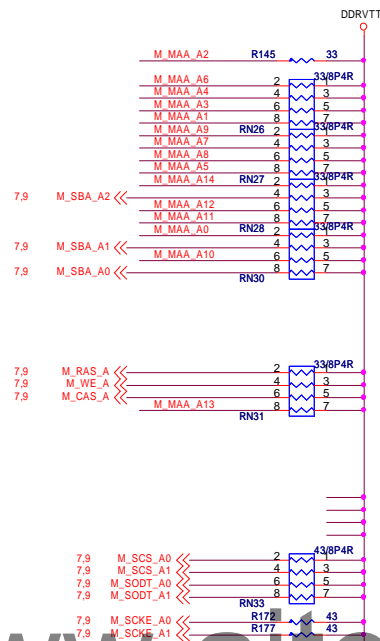
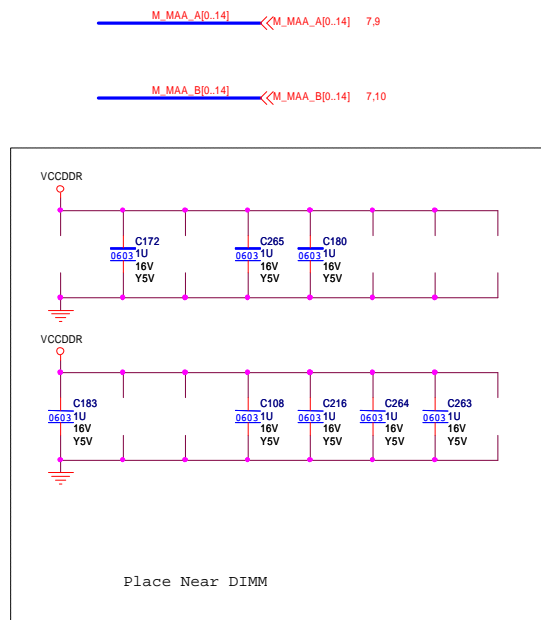
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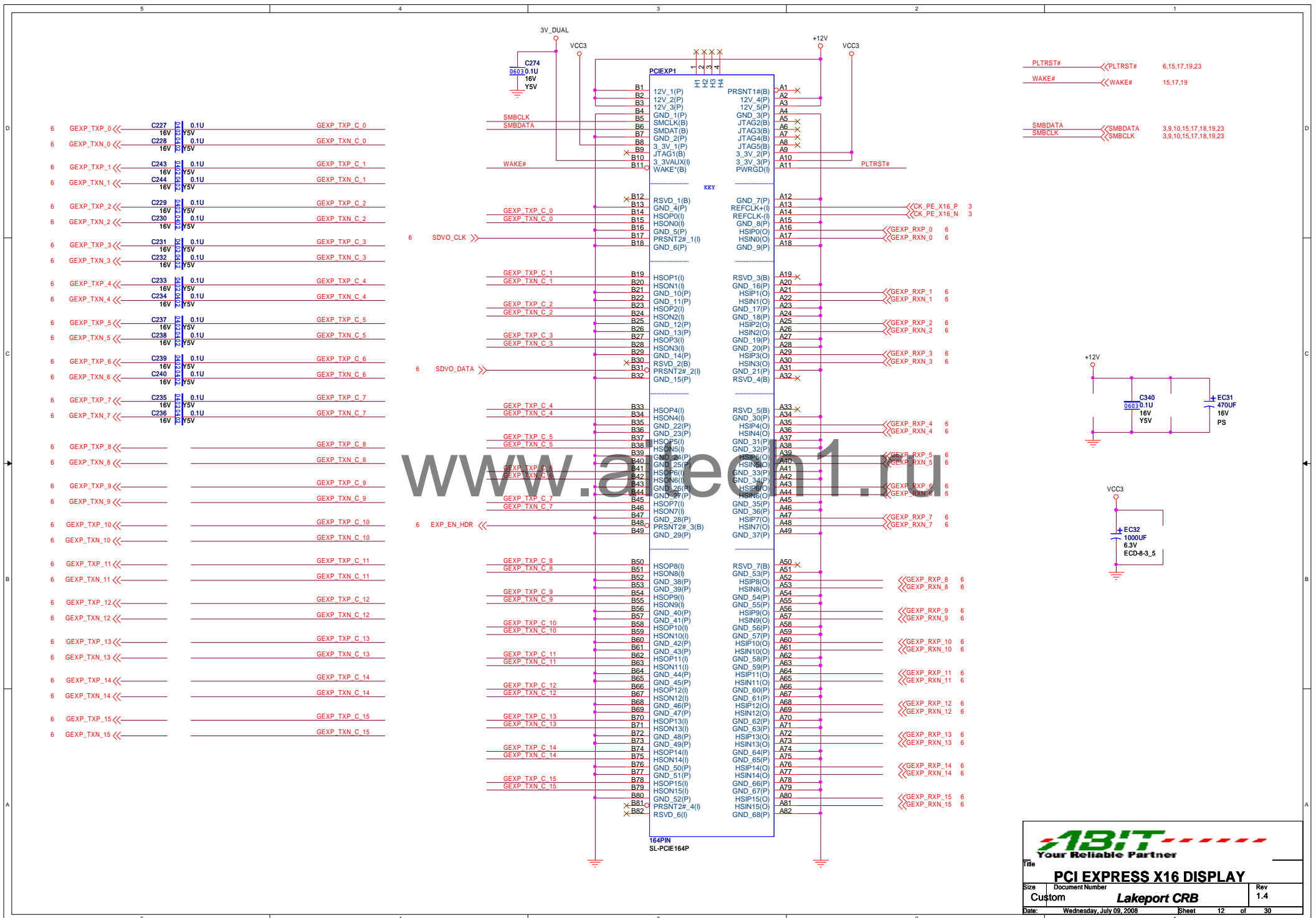


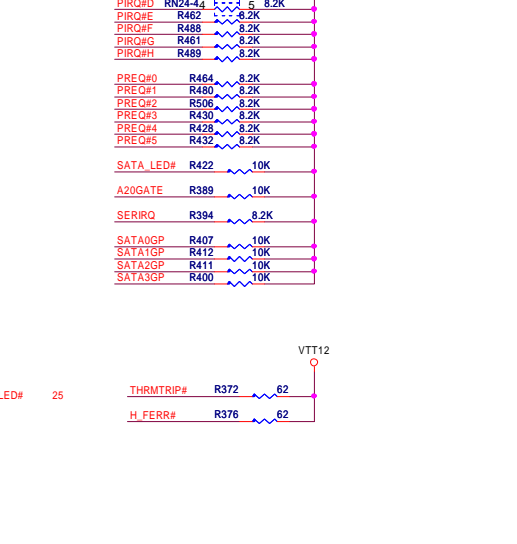
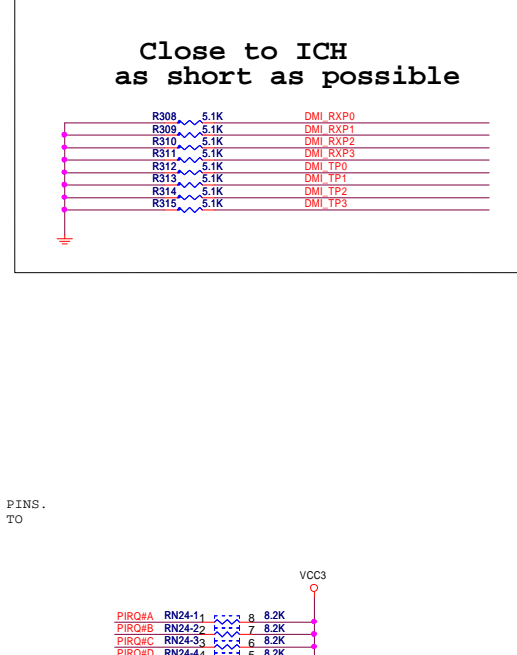
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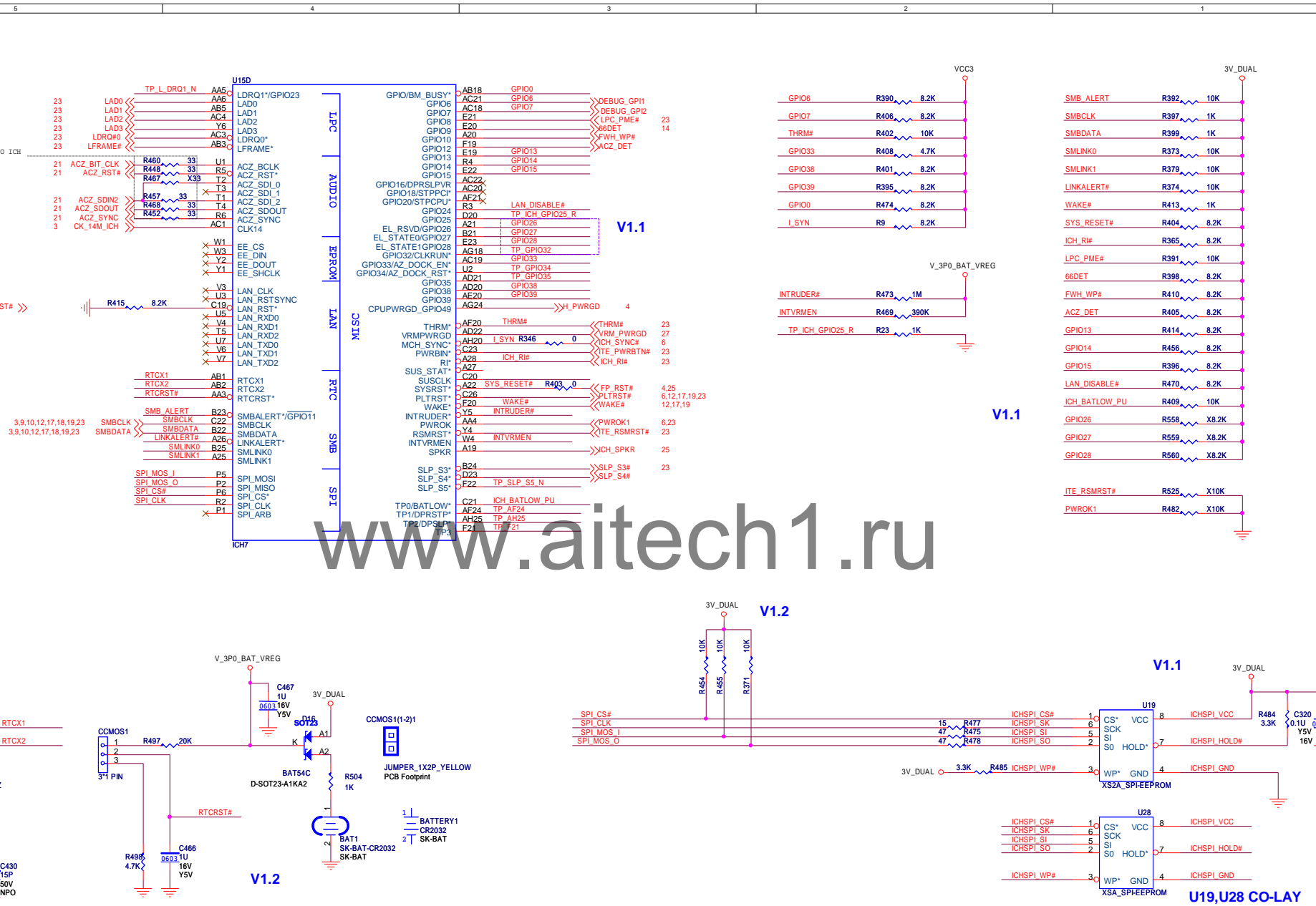
Title: **DDR II TERMINATION**

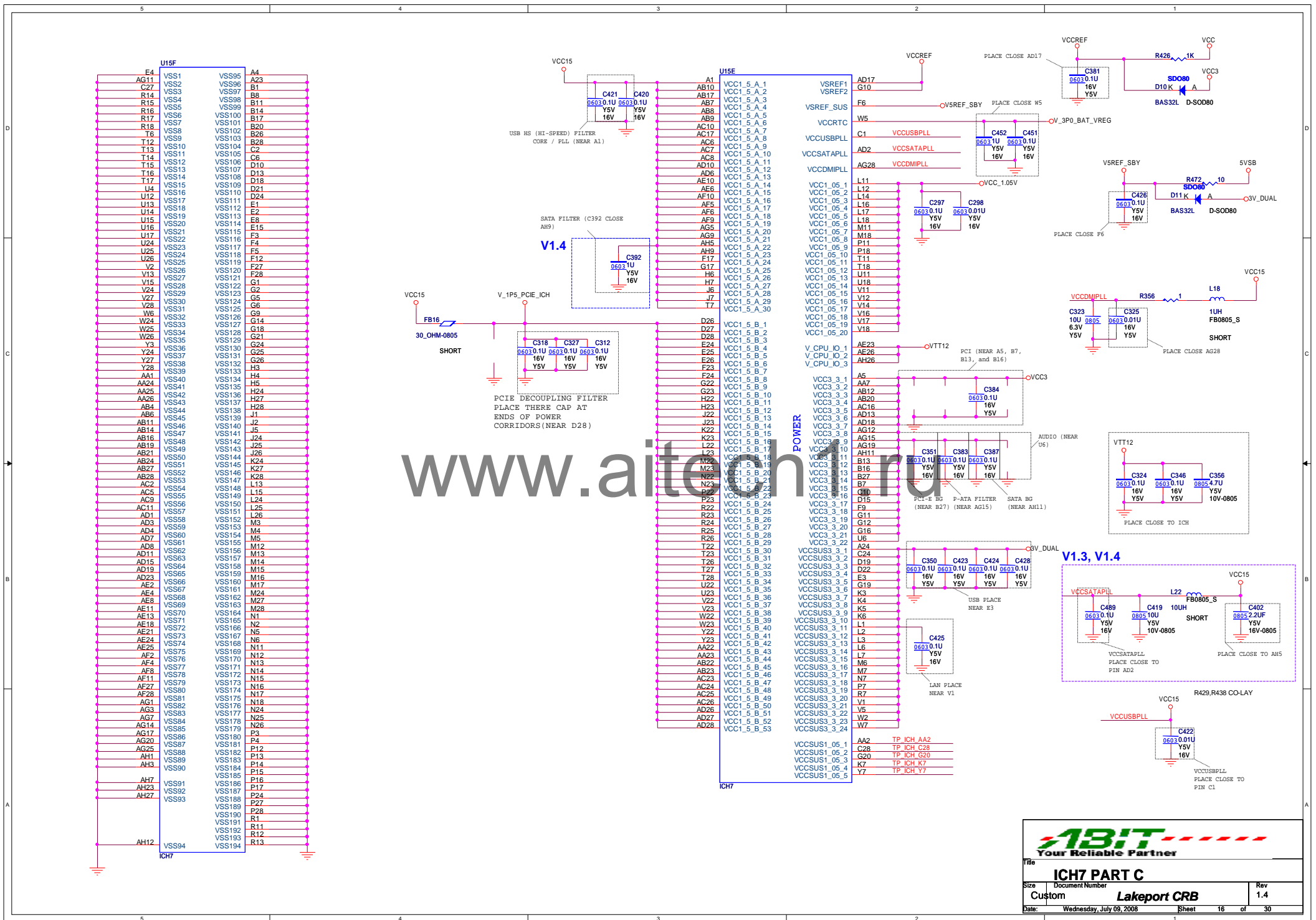
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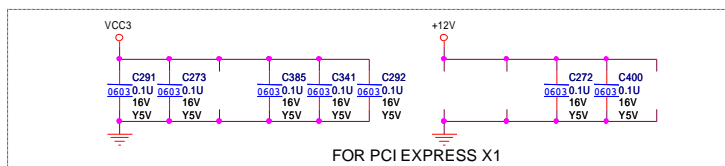
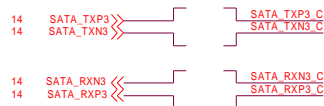
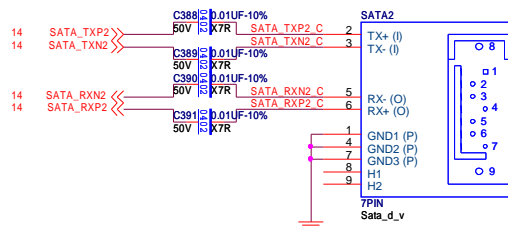
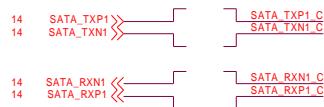
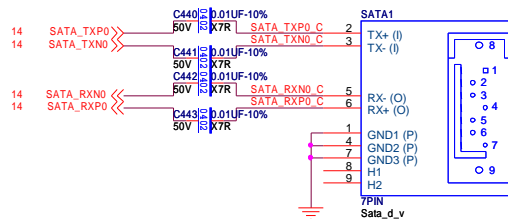
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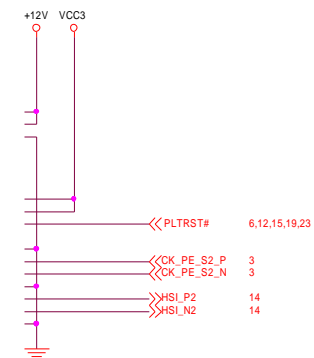
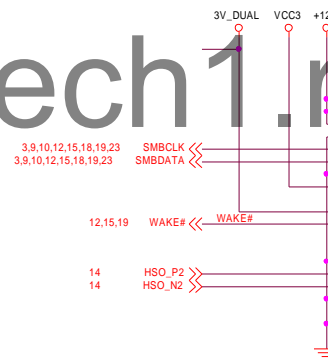








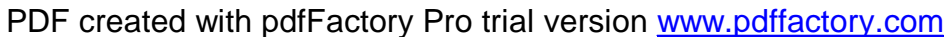
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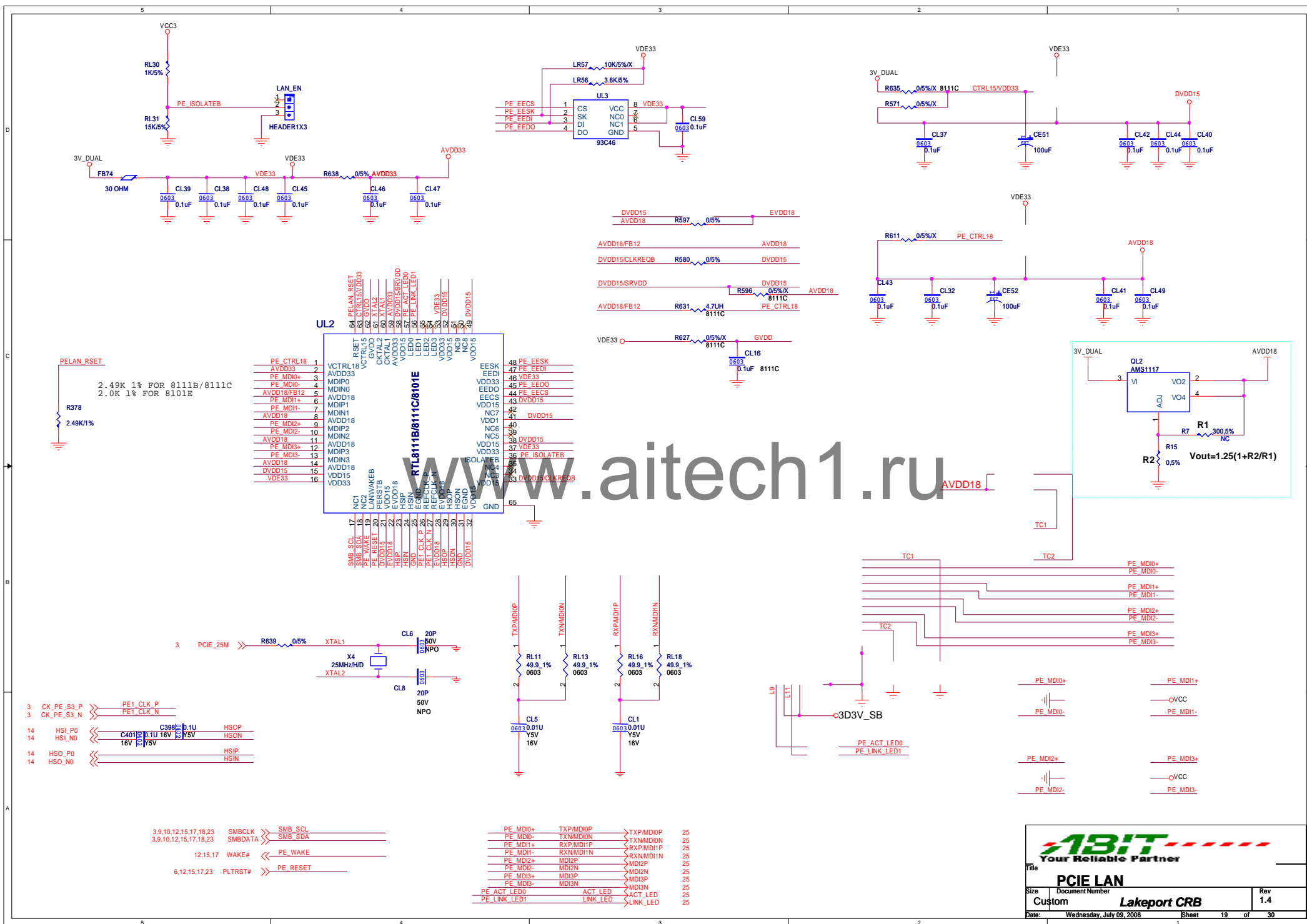


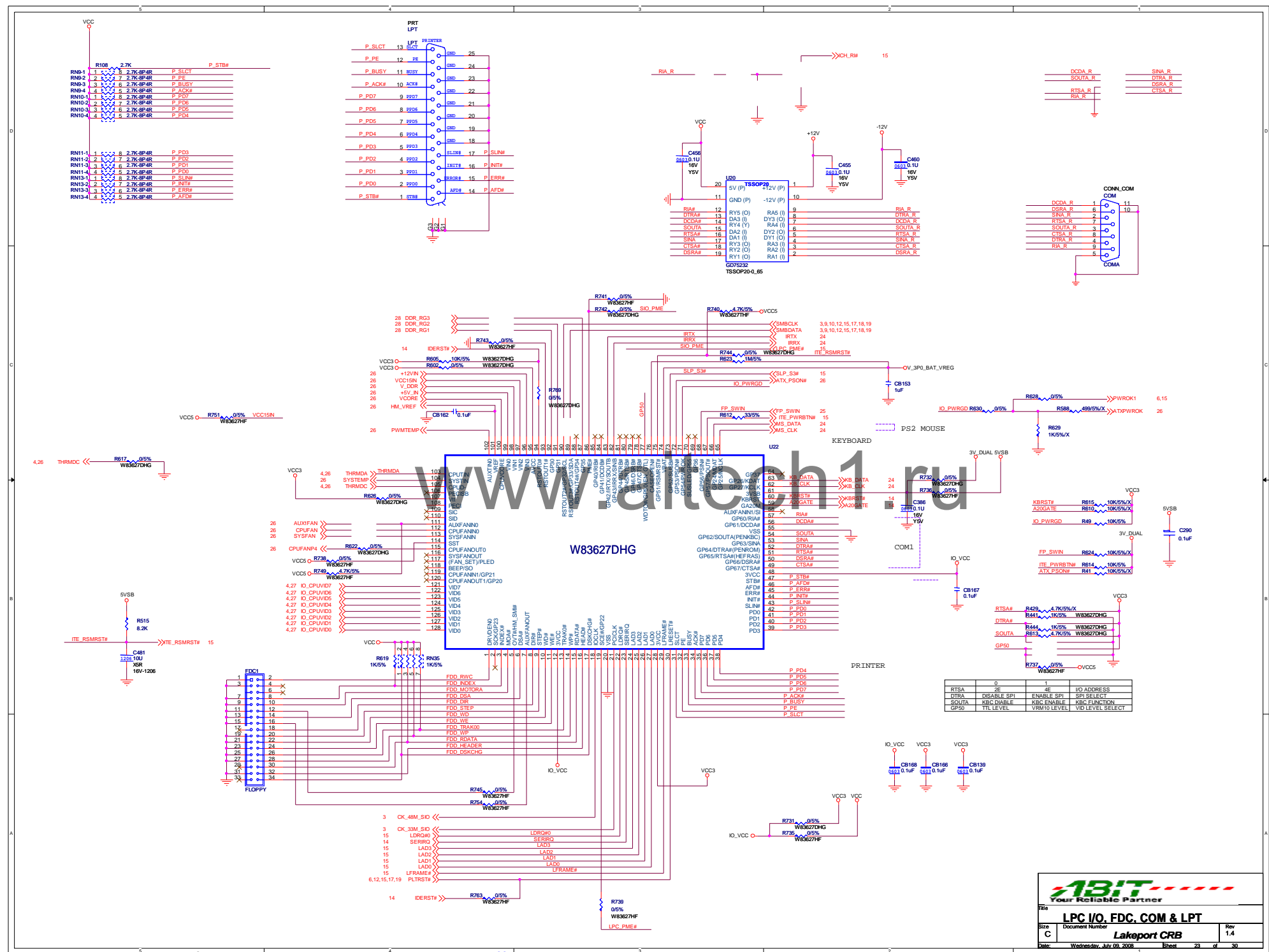
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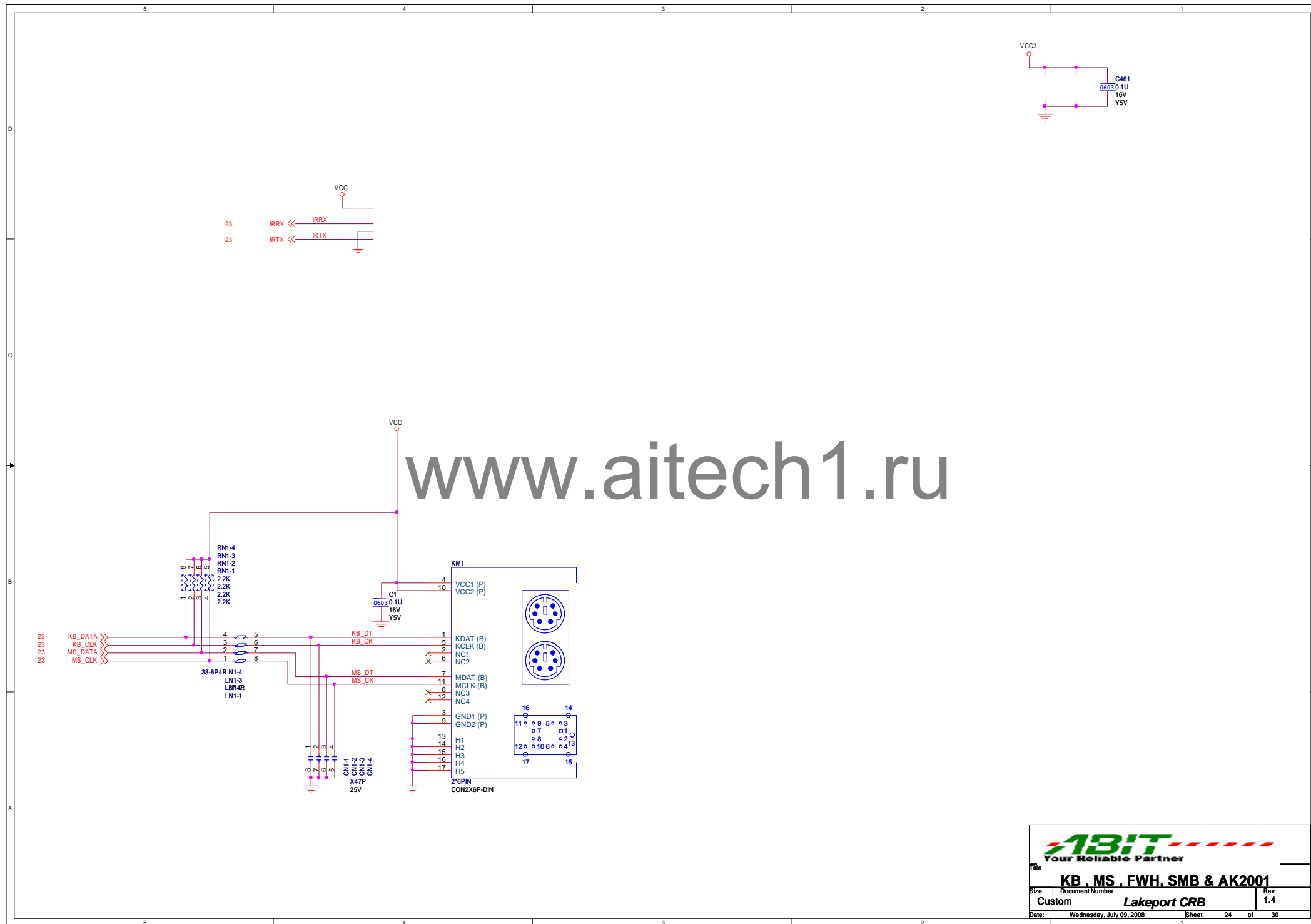
PCI EXPRESS X1 SLOT & SATA

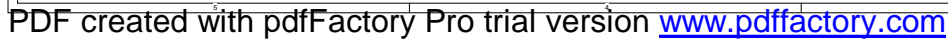
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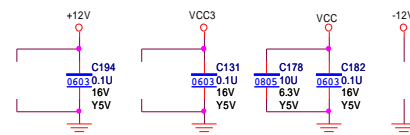




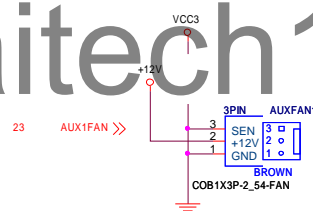




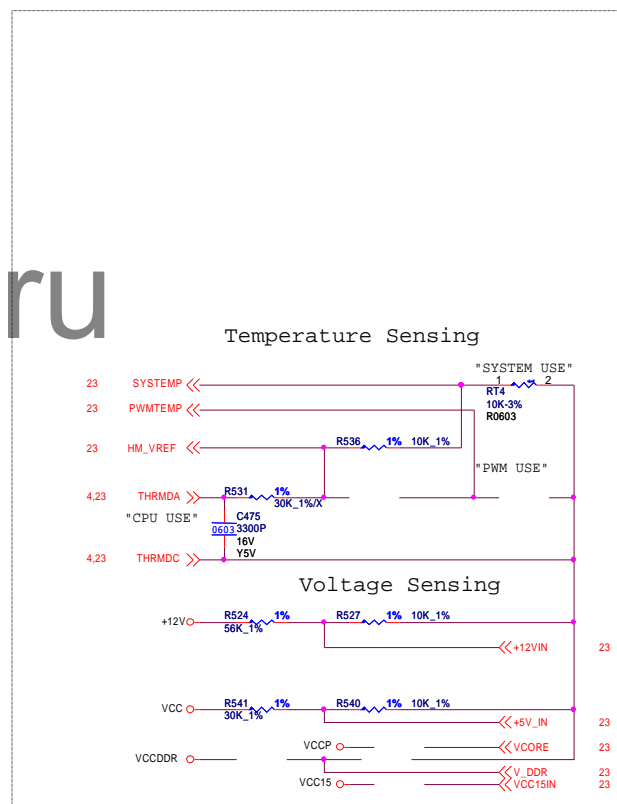
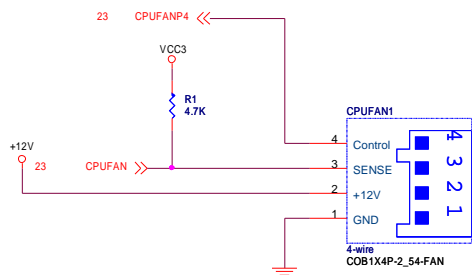




Three identical circuit diagrams are shown, each representing a different capacitor: C204, C206, and C207. Each diagram shows a vertical wire with a red circle at the top labeled 'VCC' and a red circle at the bottom labeled 'VCC3'. A capacitor symbol is placed in the middle of the wire, with the capacitor value and voltage rating written below it: '0603 0.1U 16V Y5V'.



CPU FAN



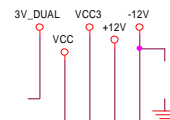
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14,18 AD[0..31] <<

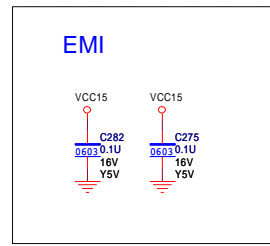
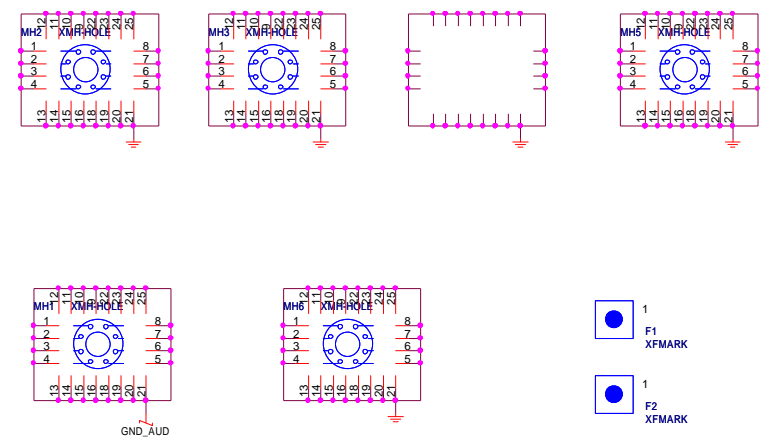
- AD0
 - AD1
 - AD2
 - AD3
 - AD4
 - AD5
 - AD6
 - AD7
 - AD8
 - AD9
 - AD10
 - AD11
 - AD12
 - AD13
 - AD14
 - AD15
 - AD16
 - AD17
 - AD18
 - AD19
 - AD20
 - AD21
 - AD22
 - AD23
 - AD24
 - AD25
 - AD26
 - AD27
 - AD28
 - AD29
 - AD30
 - AD31
- AD24
- 14,18 C_BE#0 >>
 - 14,18 C_BE#1 >>
 - 14,18 C_BE#2 >>
 - 14,18 C_BE#3 >>
 - 14,18 PIRQ#E >>
 - 14,18 PIRQ#F >>
 - 14,18 PIRQ#G >>
 - 14,18 PIRQ#H >>
 - 14 PREQ#3 >>
 - 14 PGNT#3 >>
 - 14,18 PCI_PME# >>
 - 14,18 FRAME# >>
 - 14,18 TRDY# >>
 - 14,18 STOP# >>
 - 14,18 IRDY# >>
 - 14,18 DEVSEL# >>
 - 14,18 PLOCK# >>
 - 14,18 PERR# >>
 - 14,18 PAR >>
 - 18 SMBDATA_P >> SMBDATA_P
 - 14,18 PCI_RST# >>
 - 18 SMBCLK_P >> SMBCLK_P
 - 18 REQ64#4 >> REQ64#4
 - 18 ACK64# >> ACK64#
 - 3 CK_33M_S4 >>

REQ64#4

VCC3



>>SERR# 14,18



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